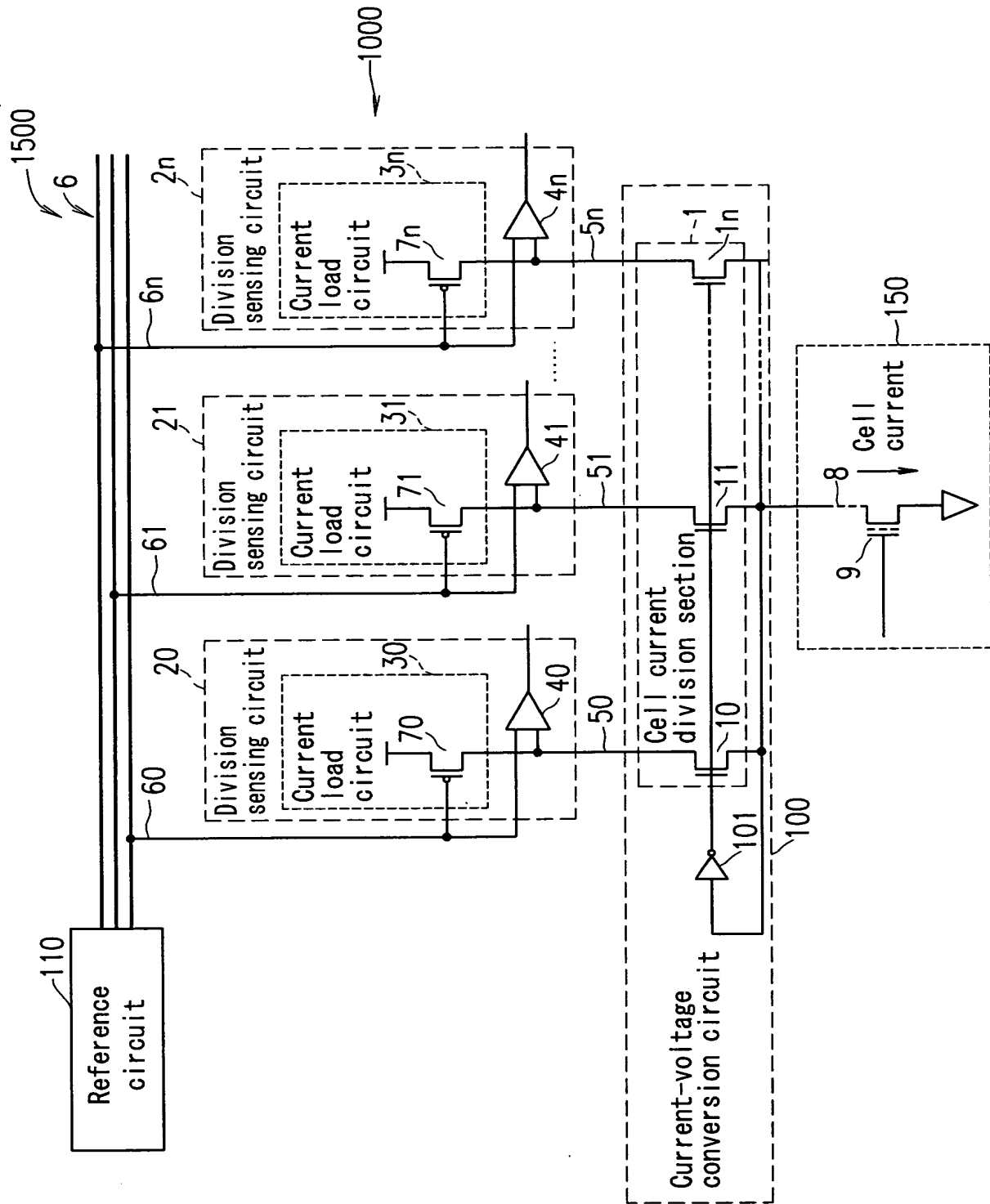
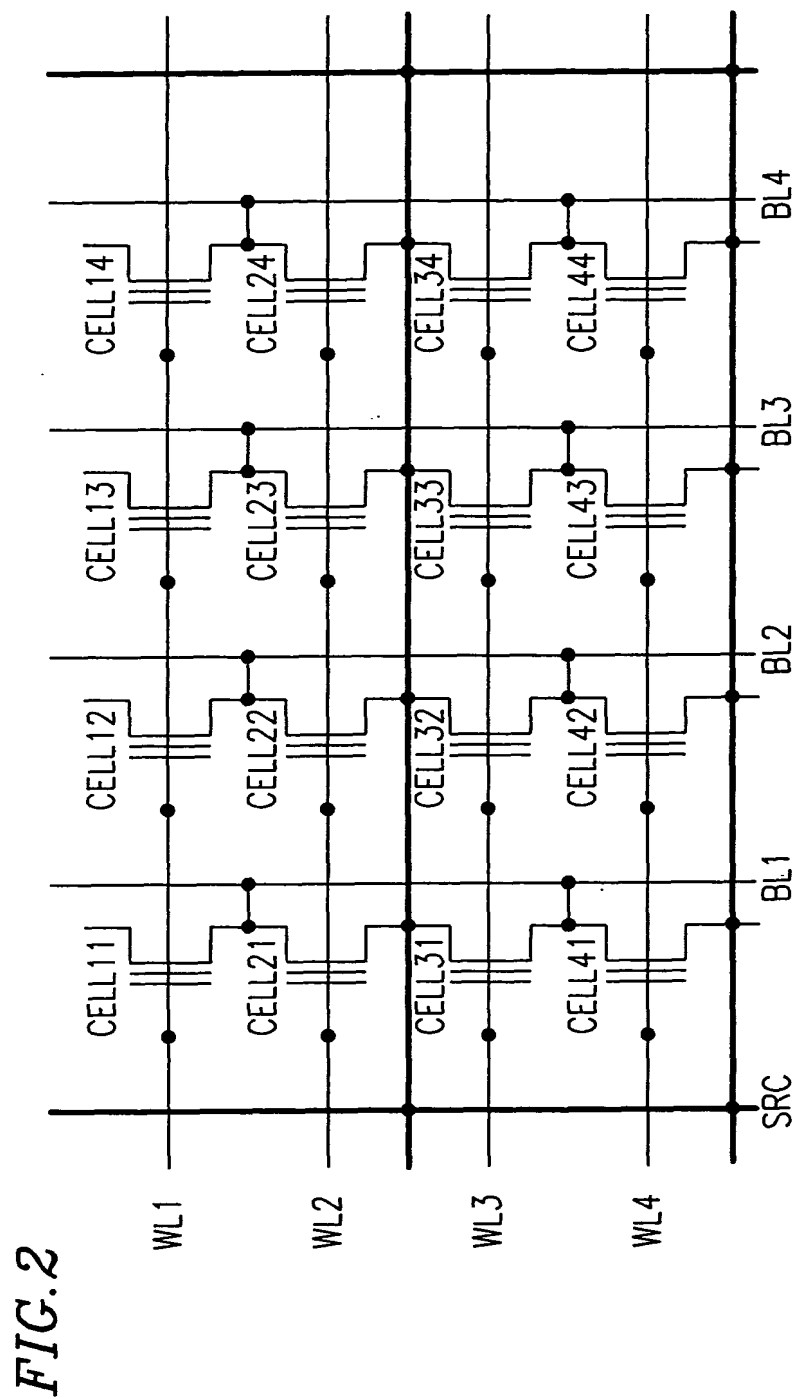
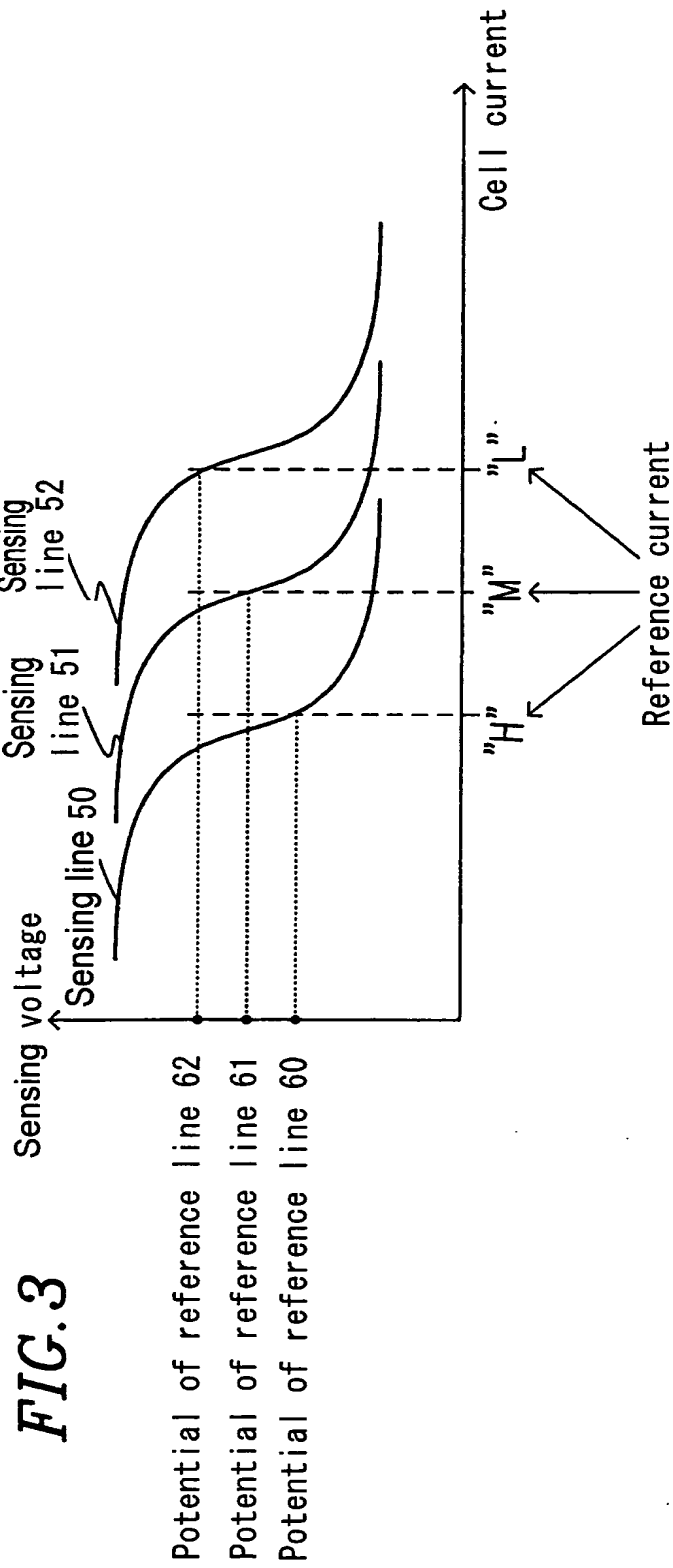


**FIG. 1**







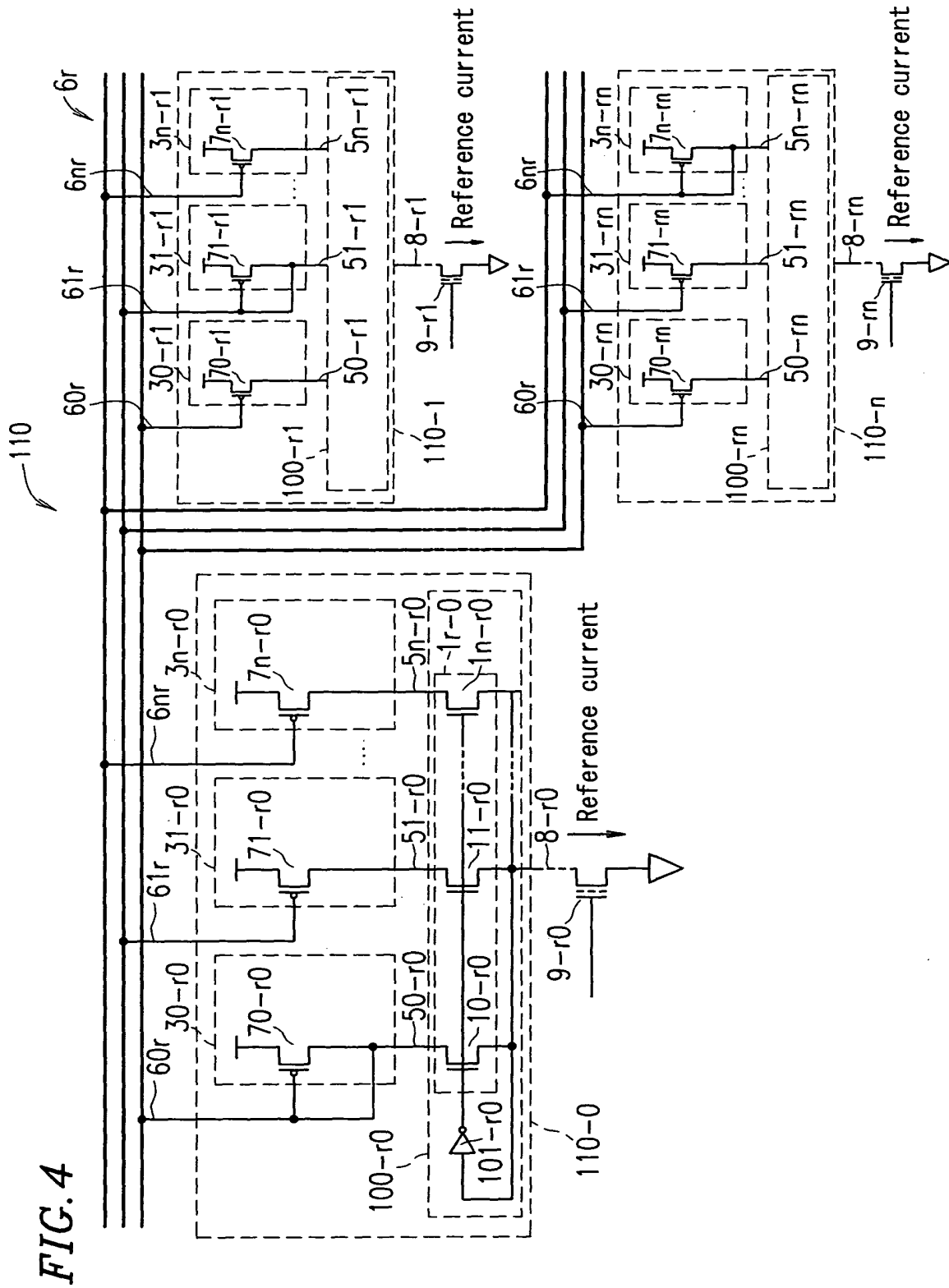
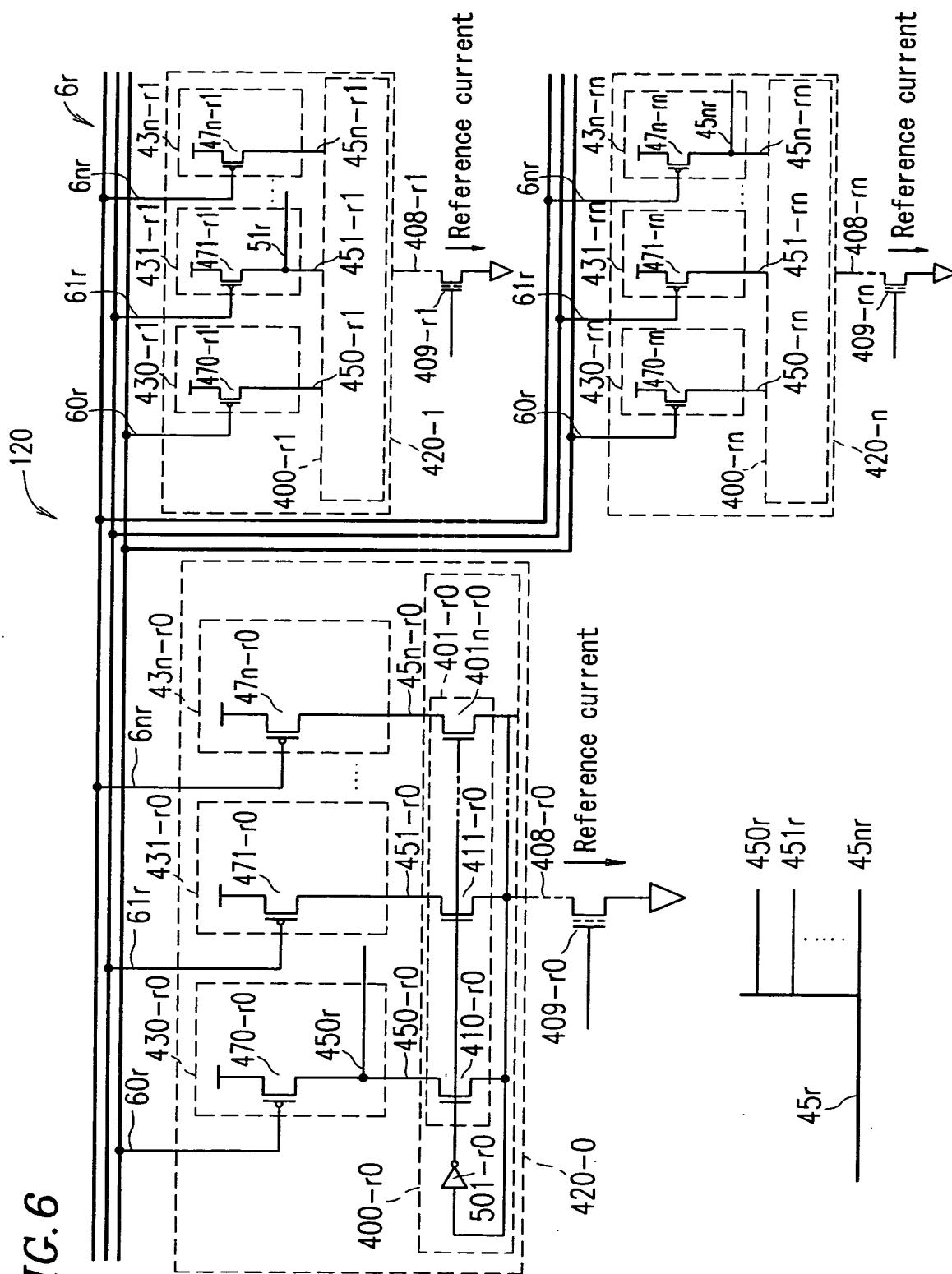




FIG. 6



**FIG. 7**

Source-drain current  
 of NMOS transistor

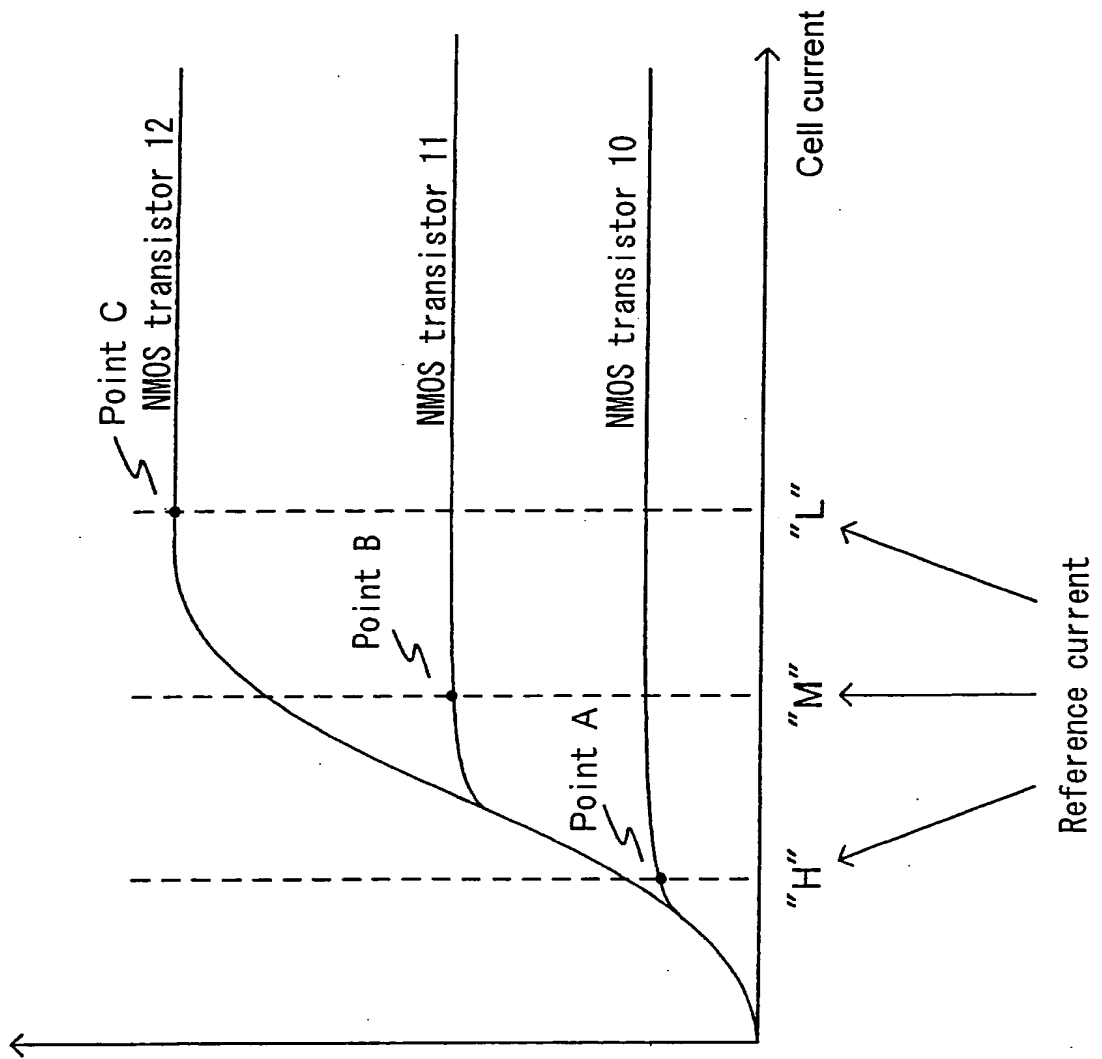


FIG. 8

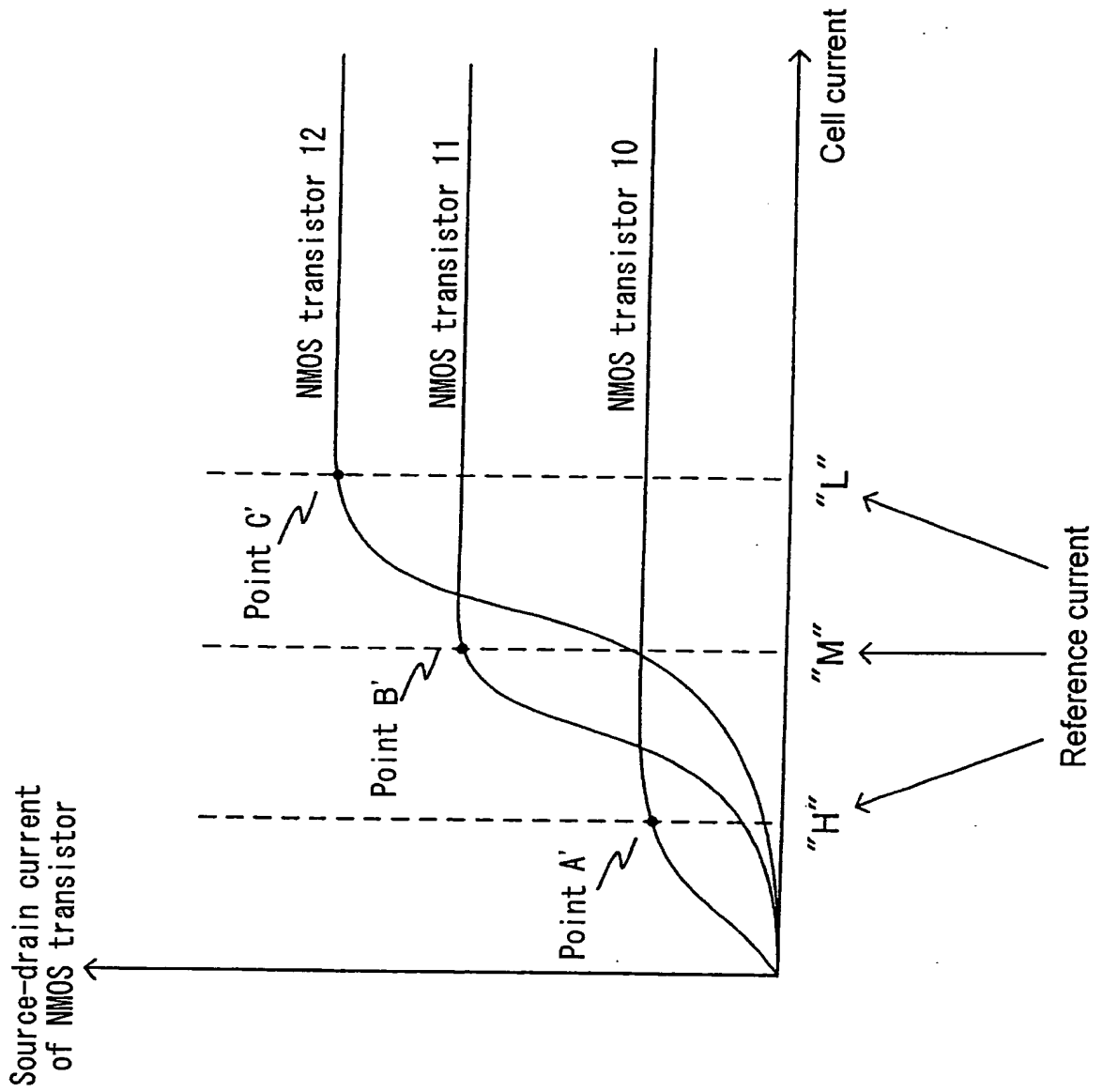


FIG. 9A

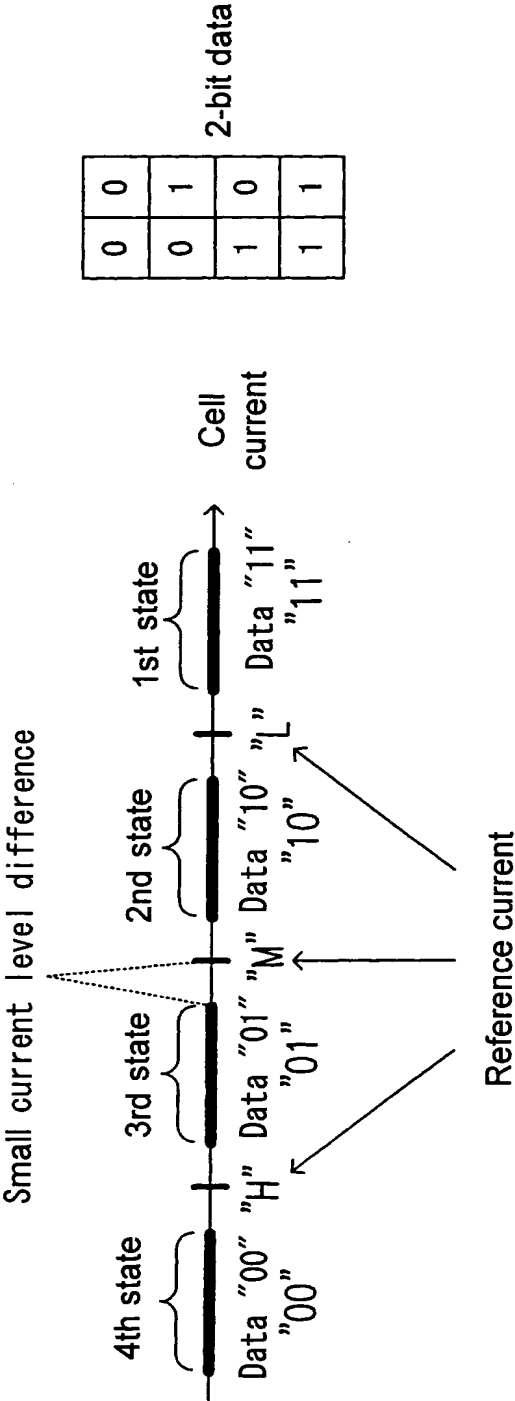
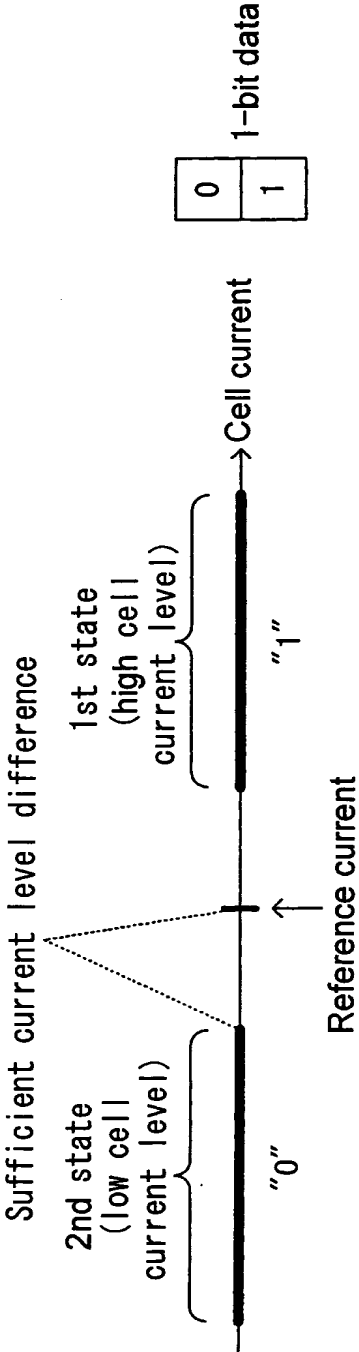
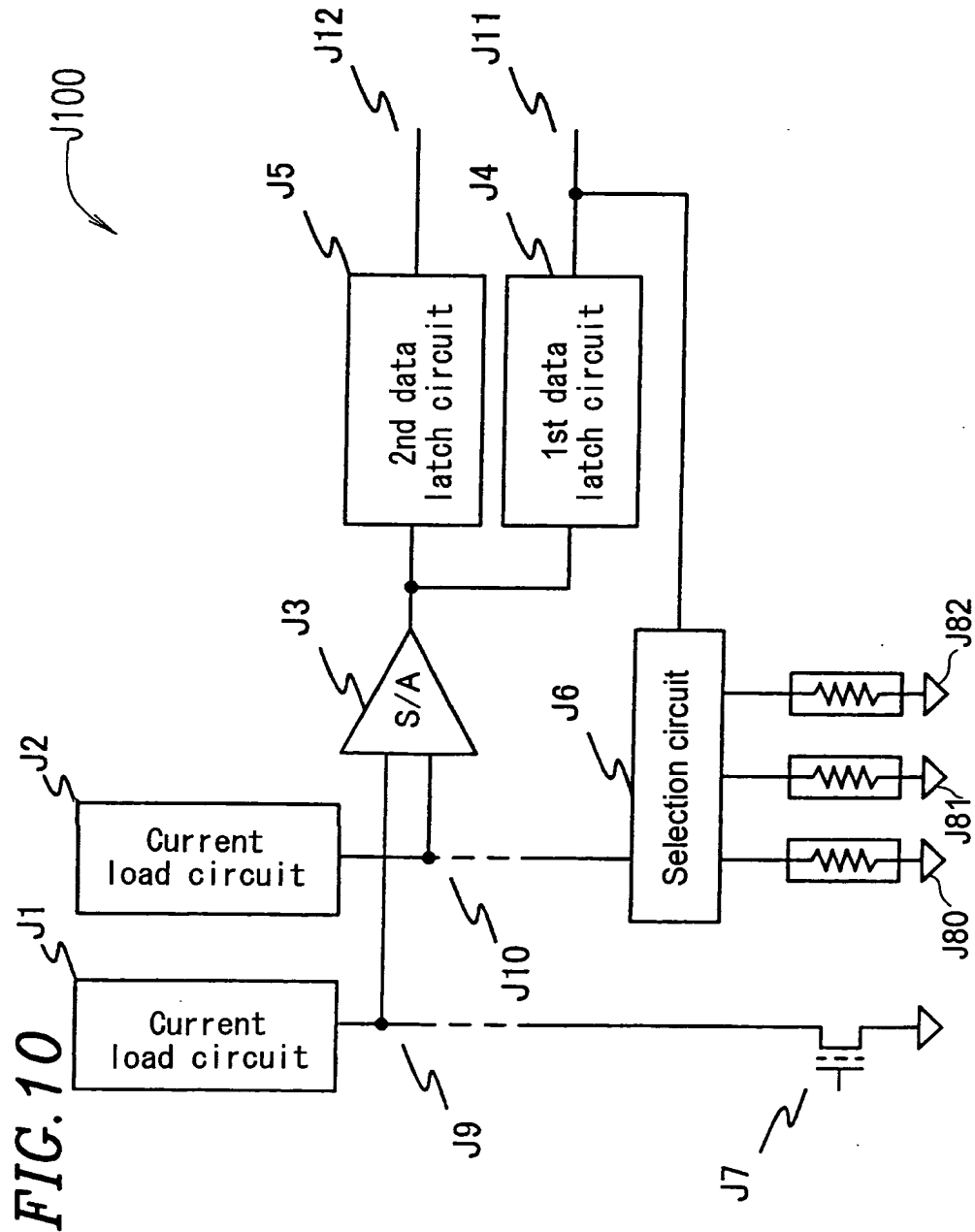


FIG. 9B





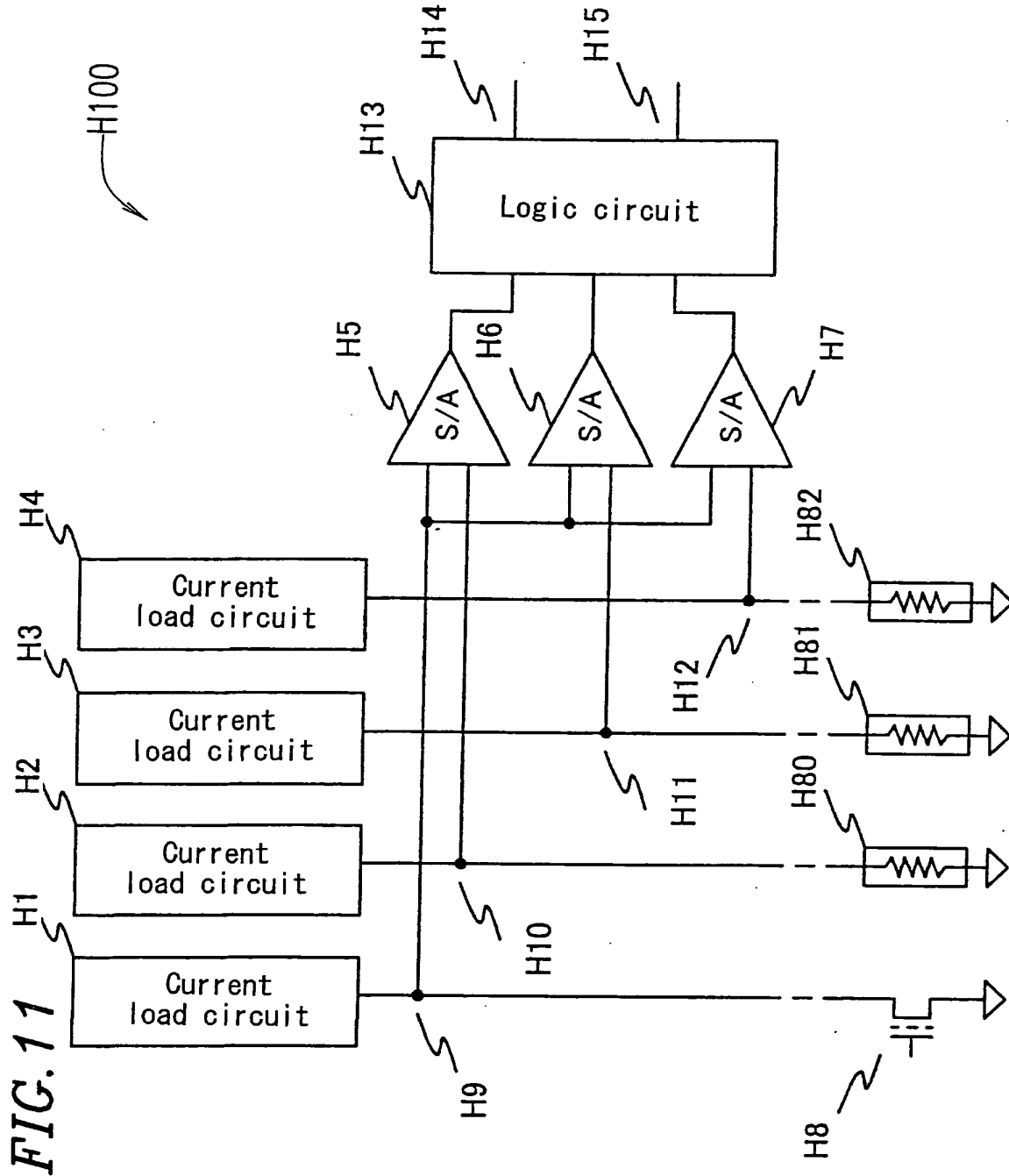


FIG. 12

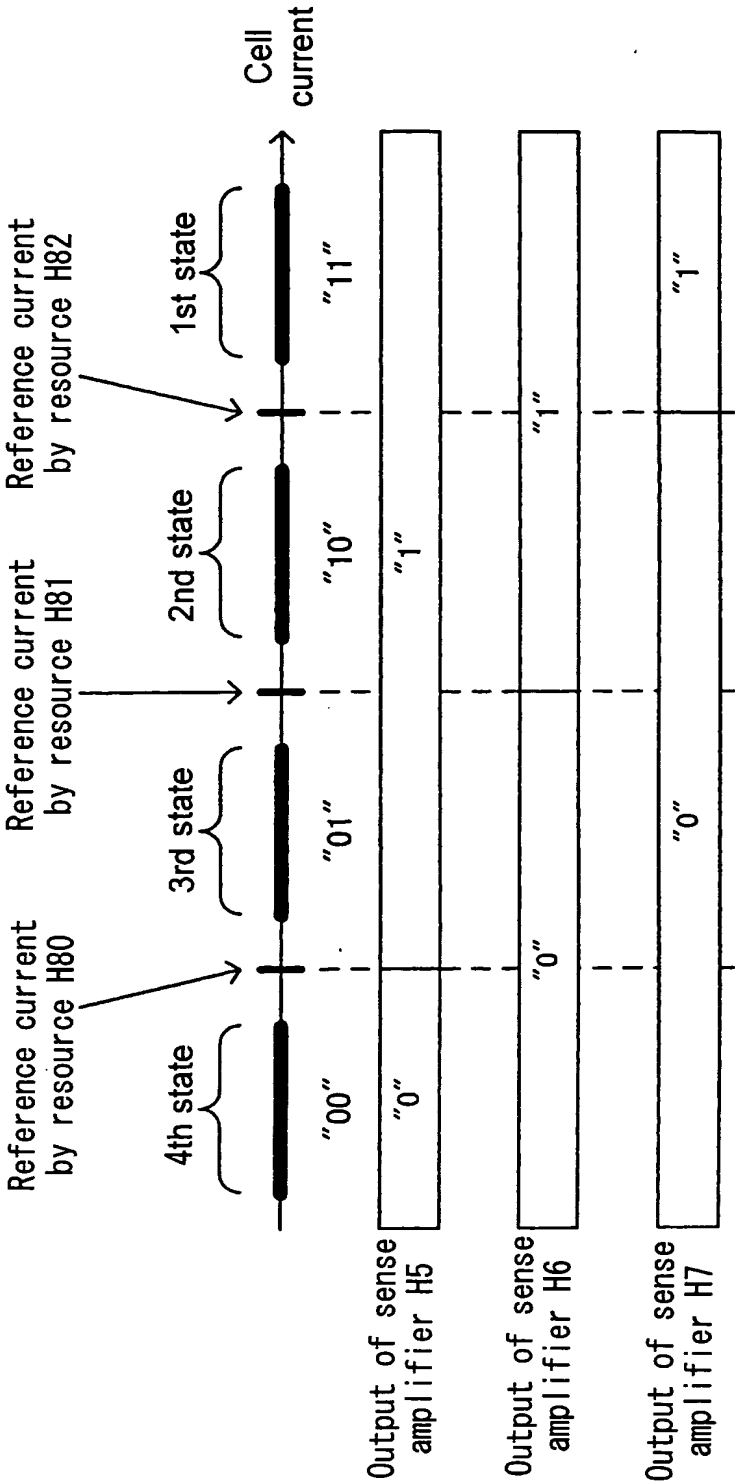


FIG. 13

Input				Output	
H5 output	H6 output	H7 output		H14	H15
0	0	0		0	0
1	0	0		0	1
1	1	0		1	0
1	1	1		1	1

FIG. 14A

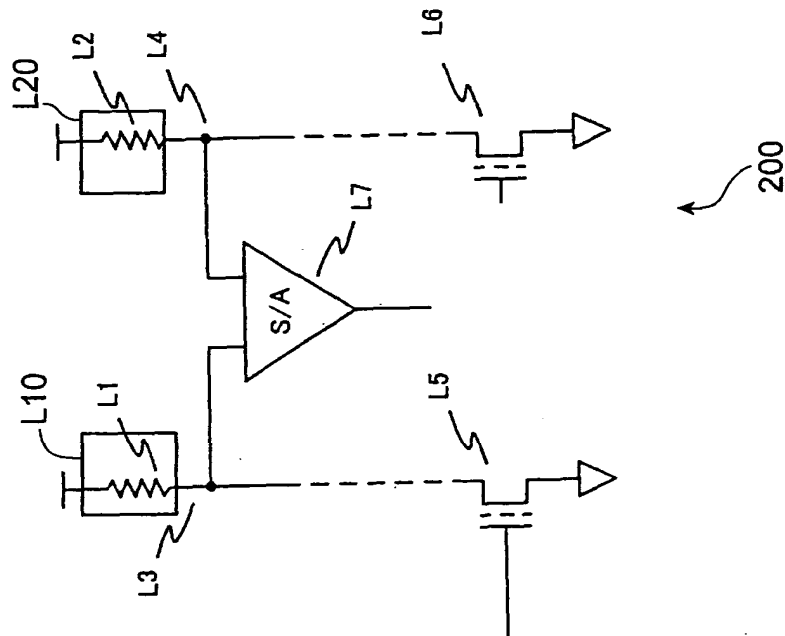


FIG. 14B

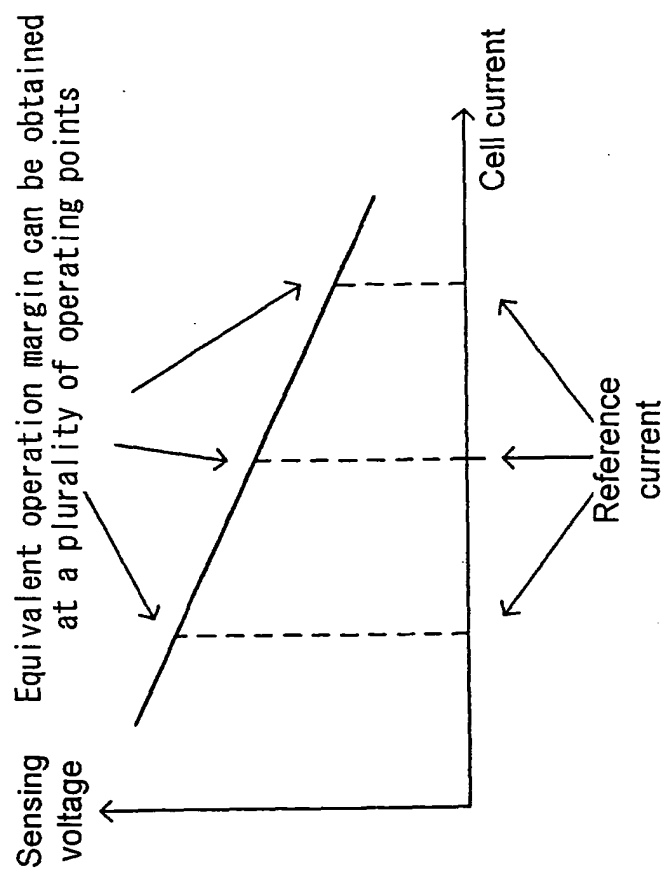
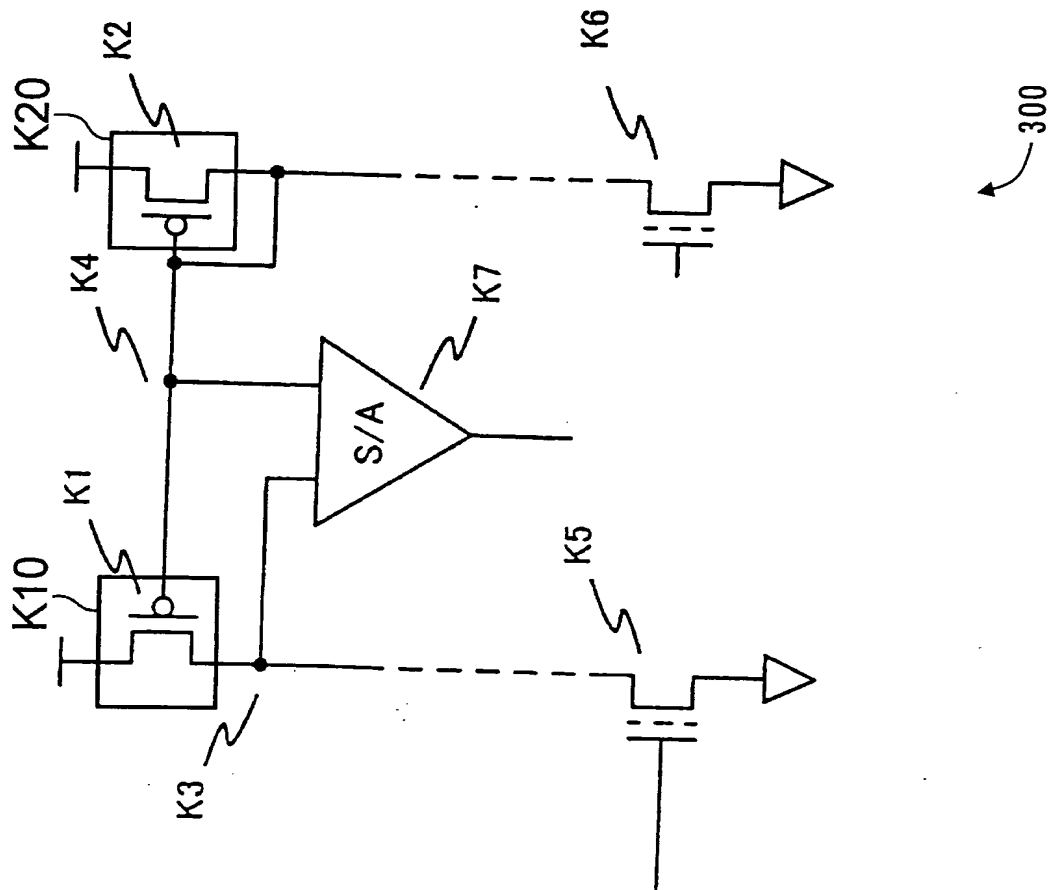


FIG. 15A



**FIG. 15B**

